

Shri Vaishnav Vidyapeeth Vishwavidyalaya, Indore (MP)

Choice Based Credit System (CBCS) Scheme


M.Tech Digital Communication

II - SEMESTER

Sr. No.	Subject Code	Name of Subject	Course Code	Teaching Scheme/ Week			Examination Scheme					Total Marks	Credits
				L	T	P	Theory			Practical			
							End Sem University Exam	Two Term Exam	Teachers Asses- ment*	End Sem University Exam	Teachers Asses- ment*		
1	MTDC201	Wireless Communication		3	1	2	60	20	20	30	20	150	5
2	MTDC202	Modern Digital Signal Processing		3	1	2	60	20	20	30	20	150	5
3	MTDC203	Advanced Communication Systems		3	1	0	60	20	20	0	0	100	4
4	MTDC214 /224/MTES 203	Elective: 1 Cryptography & E-Security 2 Antenna Theory and Design 3 FPGA Based System Design		3	1	0	60	20	20	0	0	100	4
5	MTDC215/ 225/235	Elective: 1 Digital Image Processing 2 Artificial Intelligence 3 RF and Microwave Circuit Design		3	1	0	60	20	20	0	0	100	4
6	MTDC206	Simulation Tools for Advanced Communication Systems		0	0	2	0	0	0	30	20	50	1
7	MTDC207	MATLAB Simulation & Coding for DIP Applications		0	0	2	0	0	0	30	20	50	1
8	MTDC208	Comprehensive Viva		0	0	0	0	0	0	0	50	50	4
9	MTDC209	Seminar		0	0	4	0	0	0	0	50	50	2
TOTAL				15	5	12	300	100	100	120	180	800	30

***Teacher Assessment shall be based on following components: Quiz/Assignment/Project/Participation in Class, given that no component shall exceed more than 10 marks.**


Chairperson
 Board of Studies
 Shri Vaishnav Vidyapeeth Vishwavidyalaya
 Indore


Deputy Registrar
 Shri Vaishnav Vidyapeeth Vishwavidyalaya
 Indore